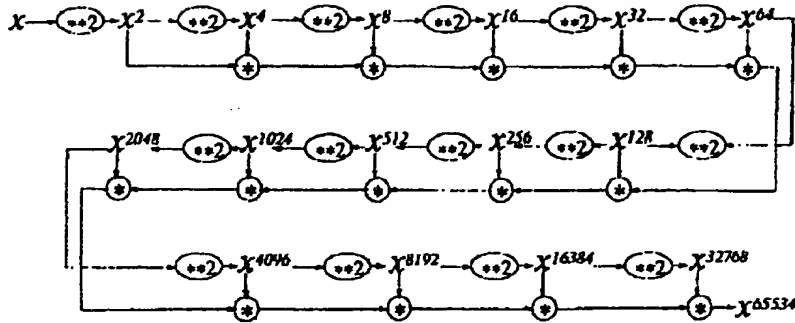
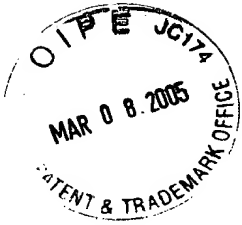


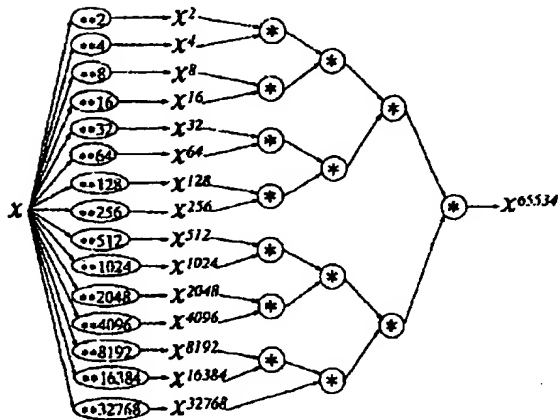
# Replacement Sheet



Example calculation process using Fermat's little theorem ( $m=16$ )

PRIOR ART

Fig. 1



Example calculation process using Fermat's little theorem ( $m=16$ )

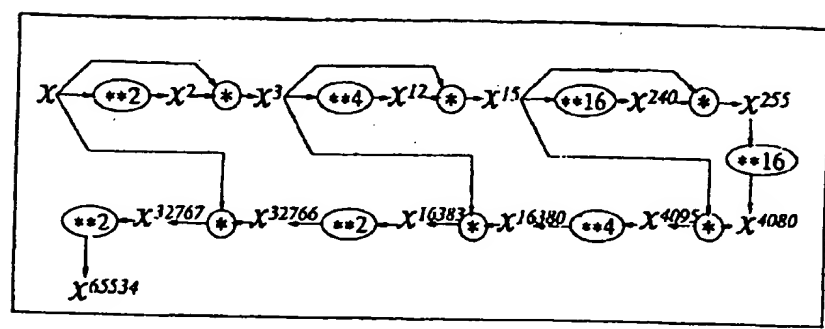
PRIOR ART

Fig. 2



Title of the Invention: Multiplication Module,  
 Multiplicative Inverse Arithmetic  
 Circuit, Multiplicative Inverse ...  
 Inventor's Name: Morioka et al.  
 Docket No./Application No.: 09/990,021

# Replacement Sheet

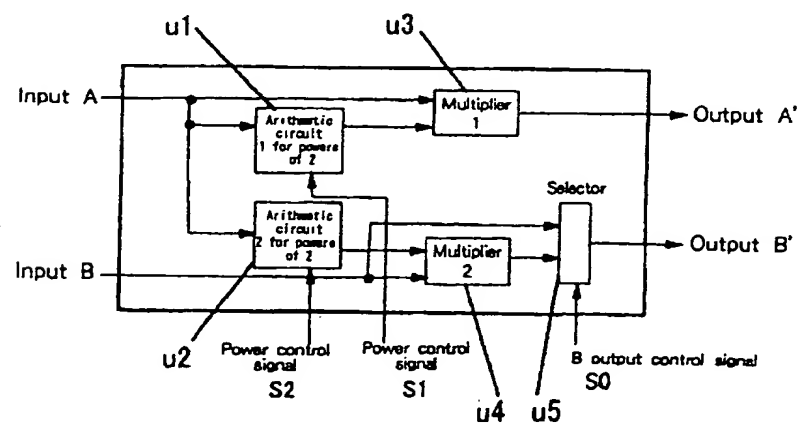


Example calculation process using the algorithm of Itoh and Tsujii (m = 16)

PRIOR ART

Fig. 3

Basic arithmetic module



Multiplication device used for the circuit configuration

Fig. 4